

In the Non-Final Office Action, Examiner Dinh rejected pending claims 1-10 on various grounds. The Applicants respond to each rejection as subsequently recited herein, and respectfully requests reconsideration and further examination of the present application under 37 CFR § 1.112:

A. Claim 10 was rejected under 35 U.S.C. §112, ¶2 as being indefinite

The Applicant cancelled claim 10 herein without prejudice or disclaimer to the subject matter of claim 10. Withdrawal of the rejection of claim 10 under 35 U.S.C. § 112, ¶ 2 is therefore respectfully requested.

B. Claims 1, 3 and 4 were rejected under 35 U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,411,045 B1 to *Nerone*, and claims 8 was rejected under 35 U.S.C. §102(e) as being anticipated by WO 01/01385 A1 to *Reymond*

The Applicant has thoroughly considered Examiner Dinh's remarks concerning the patentability of claims 1, 3 and 4 over *Nerone*, and claim 8 over *Reymond*. The Applicant has also thoroughly read *Nerone* and *Reymond*. As illustrated in FIGS. 1-4 of *Nerone*, *Nerone* fails to disclose a resonant inductor 150 and a resonant capacitor 155 connected to various LED arrays 165, 260, 310, 410 in a series resonant, series loaded configuration due to the failure of resonant capacitor 155 being connected in series between resonant inductor 150 and the LED arrays. *Nerone* additionally fails to teach or suggest a series connection of resonant capacitor 155 between resonant inductor 150 and the LED arrays.

As illustrated in FIGS. 5, 10, 11, 17-19 of *Reymond*, *Reymond* fails to disclose a resonant inductor L and a resonant capacitor C connected to various LED arrays 31, 131, etc. in a series resonant, series loaded configuration. *Reymond* additionally fails to teach or suggest a series connection of resonant capacitor L and resonant capacitor C to the LED arrays.

By comparison, the present invention discloses (1) an inductor L1 and a capacitor C2 connected in a series resonant, series load configuration to a LED array 40a as illustrated in FIG. 2, (2) inductor L1 and capacitor C2 connected in a series resonant, series load configuration to a LED array 40b as illustrated in FIG. 3, (3) inductor L1 and each capacitor C2-C4 connected in a series resonant, series load configuration to LED₁-LED_n, respectively, of a LED array 40c as illustrated in FIG. 4, (4) inductor L1 and a capacitor array 31a connected in a series resonant, series load configuration to a LED array 40d as illustrated in FIG. 5, and (5) inductor L1 and a capacitor array 31b connected in a series resonant, series load configuration to LED array 40d as illustrated in FIG. 6.

To better differentiate the present invention of *Nerone, Reymond* and the remaining art of record, the Applicant has cancelled claims 1-10 herein and added claims 11-30. The Applicant respectfully asserts that *Nerone, Reymond* and the remaining art of record, alone and in combination, fail to disclose, teach or suggest the following limitations of independent claims 11, 15, 22 and 29:

1. "a first impedance circuit including a first inductor and a first capacitor connected to said first LED array in a first series resonant, series loaded configuration" as recited in independent claim 11;
2. "a first impedance circuit including a first inductor and a first capacitor array connected to said first LED array in a first series resonant, series loaded configuration" as recited in independent claim 15;
3. "a first impedance circuit connected to said first LED array in a first series resonant, series loaded configuration" as recited in independent claim 22; and
4. "an impedance means connected to each LED array in a series resonant, series loaded configuration" as recited in independent claim 29.

The Applicant therefore respectfully requests a withdrawal of the rejection of claims 1, 3 and 4 under 35 U.S.C. §102(e) as being anticipated by *Nerone*, a withdrawal of the rejection of claim 8 under 35 U.S.C. §102(e) as being anticipated by *Reymond*, and an allowance of claims 11-30 over *Nerone*, *Reymond* and the remaining art of record.

- C. Claims 2 and 5-7 were rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,411,045 B1 to *Nerone* in view of WO 01/01385 A1 to *Reymond*

The Applicant has cancelled claims 2 and 5-7 herein without disclaimer or prejudice as to the claimed subject matter. Withdrawal of the rejection of claims 2 and 5-7 under §103(a) as being unpatentable over *Nerone* in view of *Reymond* is therefore respectfully requested.

SUMMARY

Examiner Dinh's §§102(e), 103(a) and 112. ¶2 rejections of claims 1-10 have been obviated by the cancellation of claims 1-10. The Applicant has supported the patentability of new claims 11-30 over *Nerone, Raymond* and the remaining art of record. The Applicant respectfully submits that claims 11-30 as added herein fully satisfy the requirements of 35 U.S.C. §§ 102, 103 and 112. In view of the foregoing, favorable consideration and early passage to issue of the present application is respectfully requested. If any points remain in issue that may best be resolved through a personal or telephonic interview, Examiner Dinh is respectfully requested to contact the undersigned at the telephone number listed below.

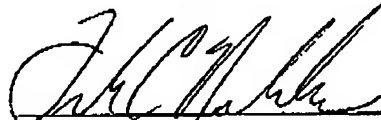
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